

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT:	Frederic BOUTAUD	GROUP:	2181
SERIAL NO:	10/715,629	EXAMINER:	V. Lai
FILED:	November 17, 2003	CONFIRMATION:	4322
FOR:	DIGITAL SIGNAL PROCESSOR ARCHITECTURE WITH OPTIMIZED MEMORY ACCESS FOR CODE DISCONTINUITY (As Amended)		

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450
Sir:

RESPONSE UNDER 37 C.F.R. 1.116

In response to the Final Office Action mailed July 28, 2006, the following remarks are respectfully submitted under 37 C.F.R. 1.116 in connection with the above-identified application.